

100

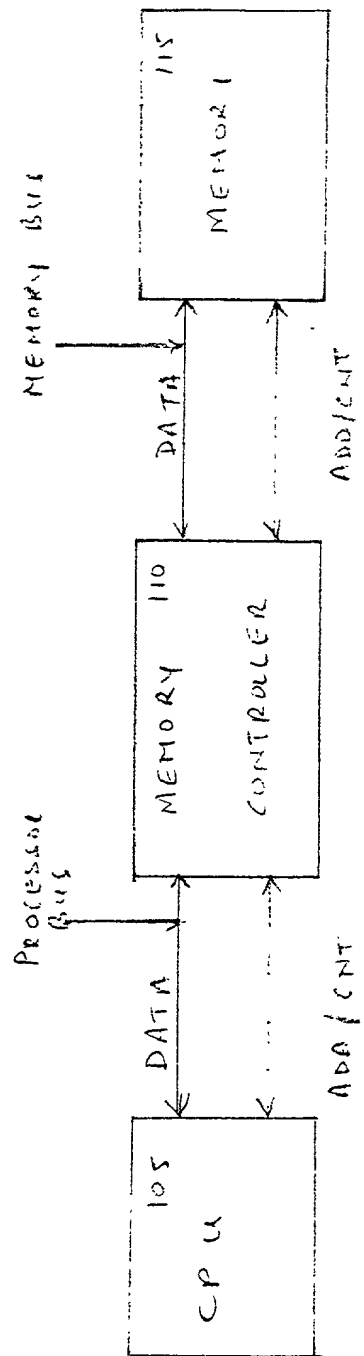


FIGURE 1

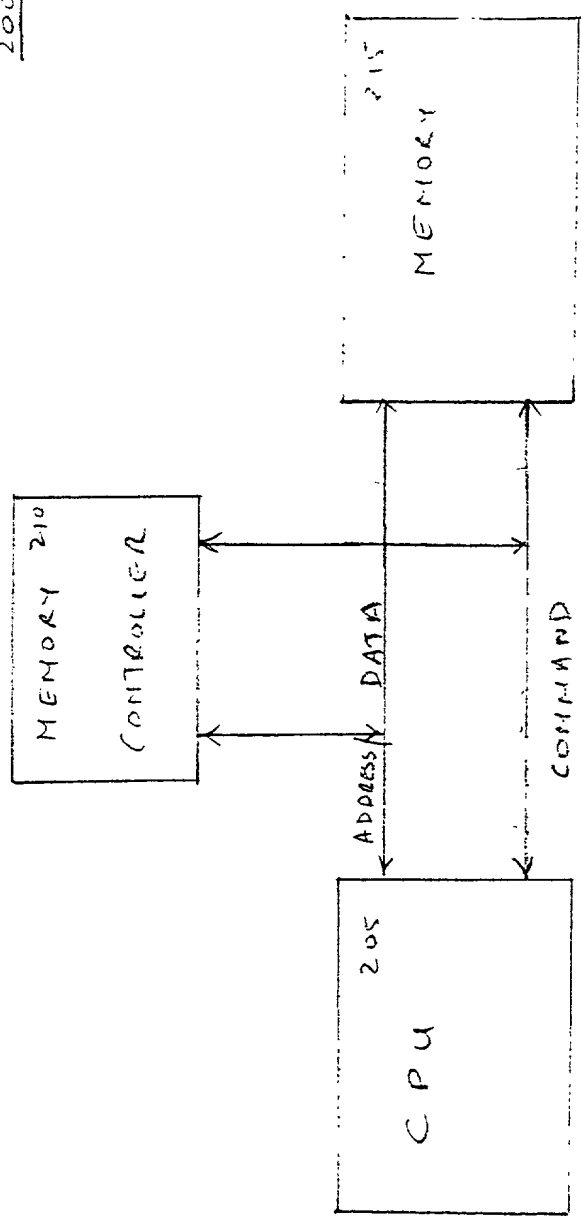


FIGURE 2

CPU Address / Data → ADDR → ADDR 305 (CPU) → ADDR 310 DECODE → CHIP SELECT, BANK SELECT, SDRAM ADDR

UPDATE ADDR TAGS

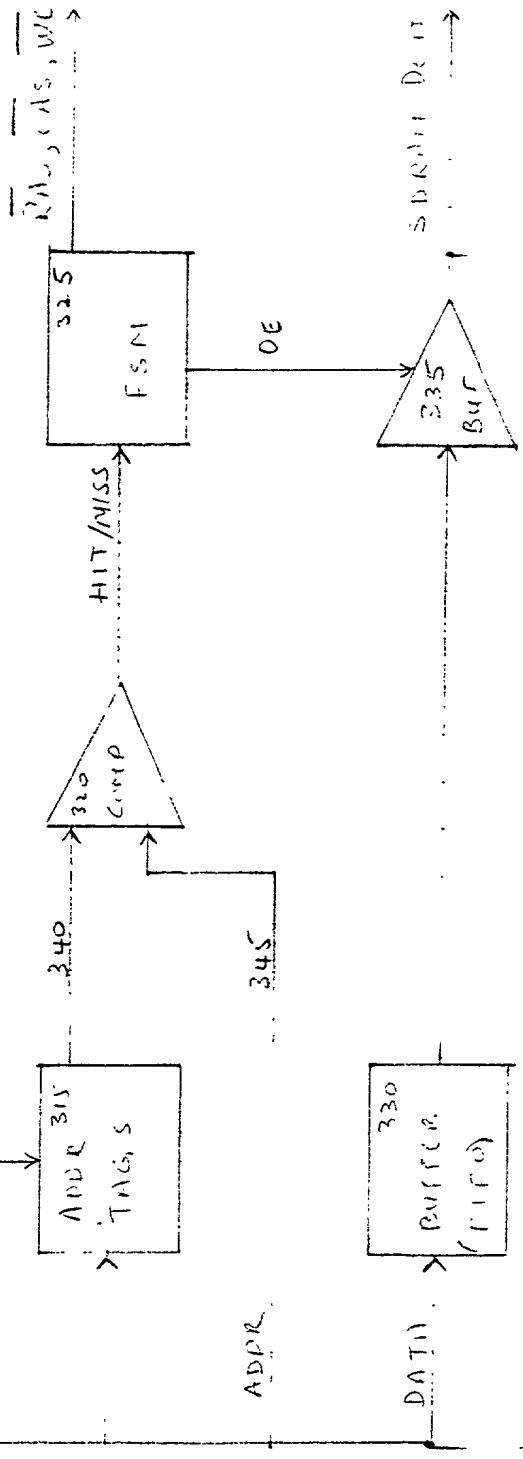


FIGURE 3

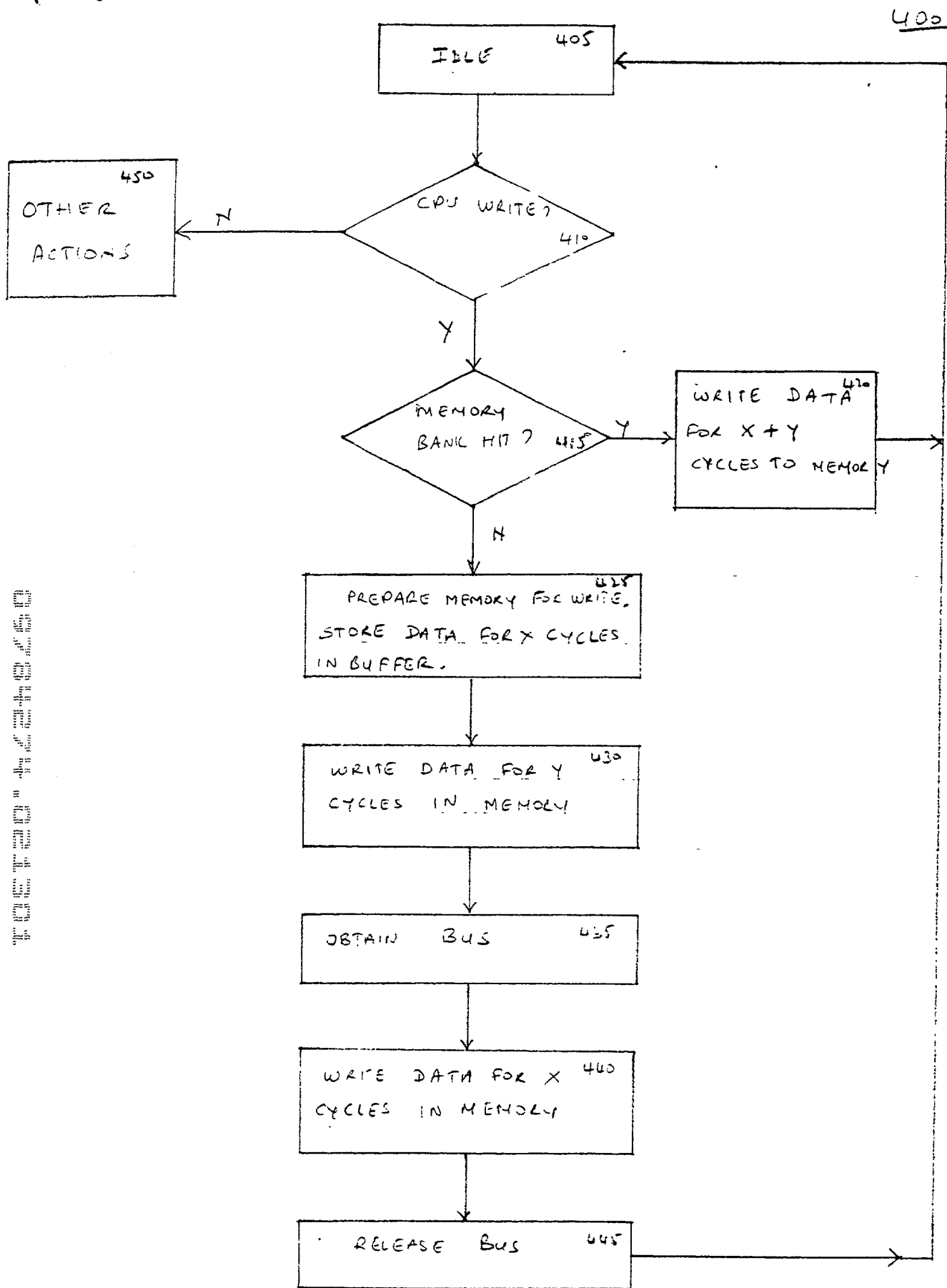


FIGURE 4

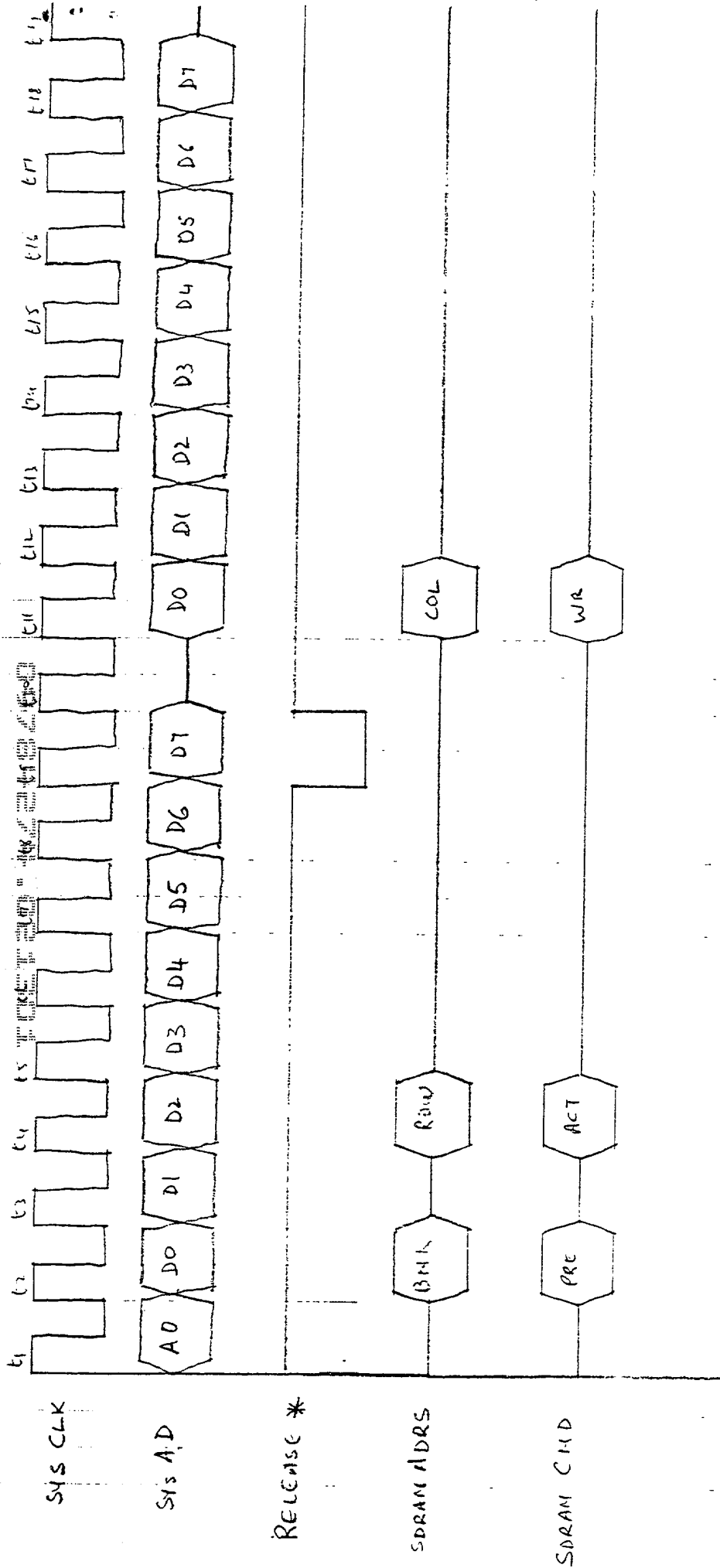


FIGURE 5 (PRIOR ART)

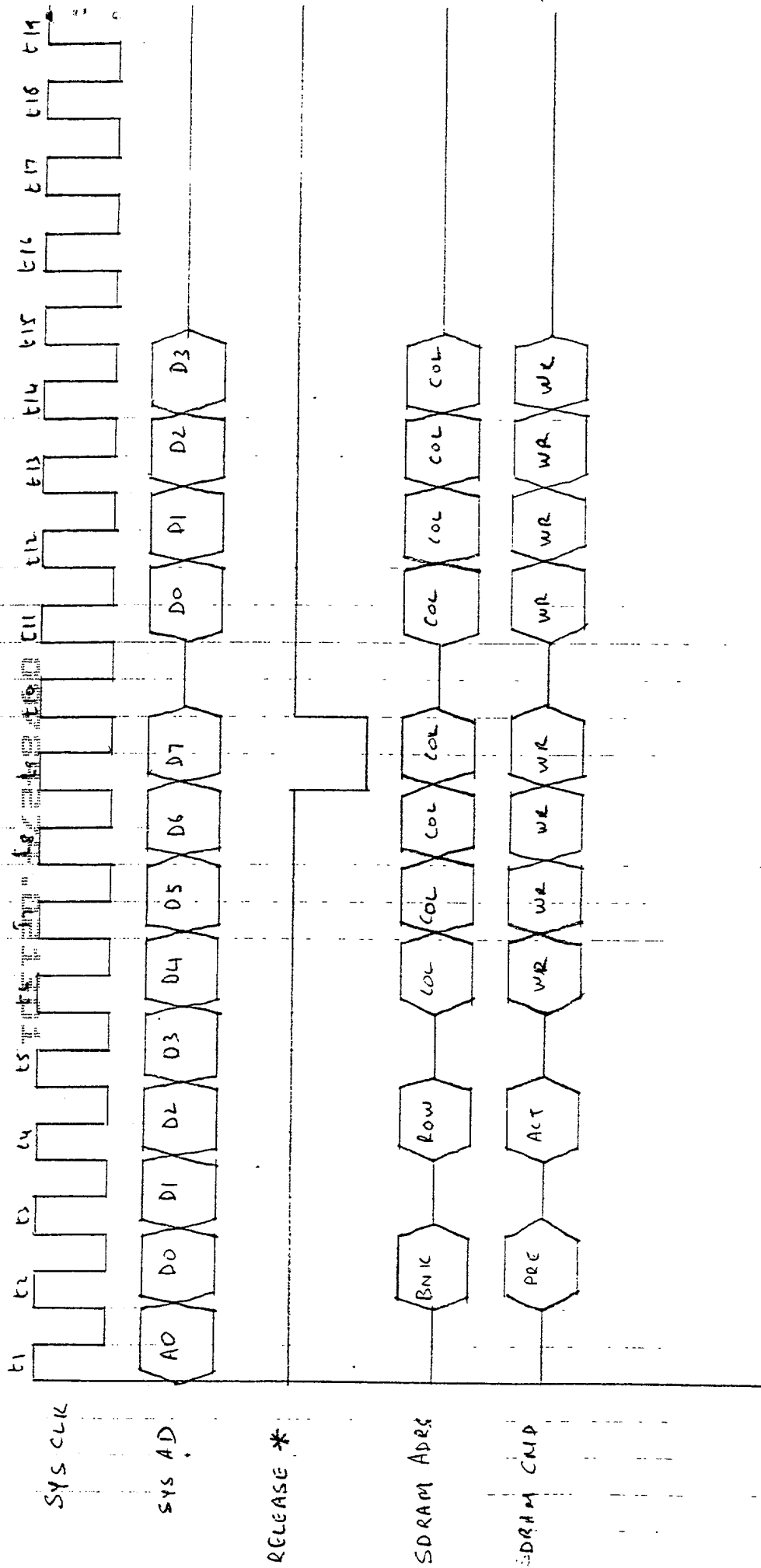


FIGURE 6

700

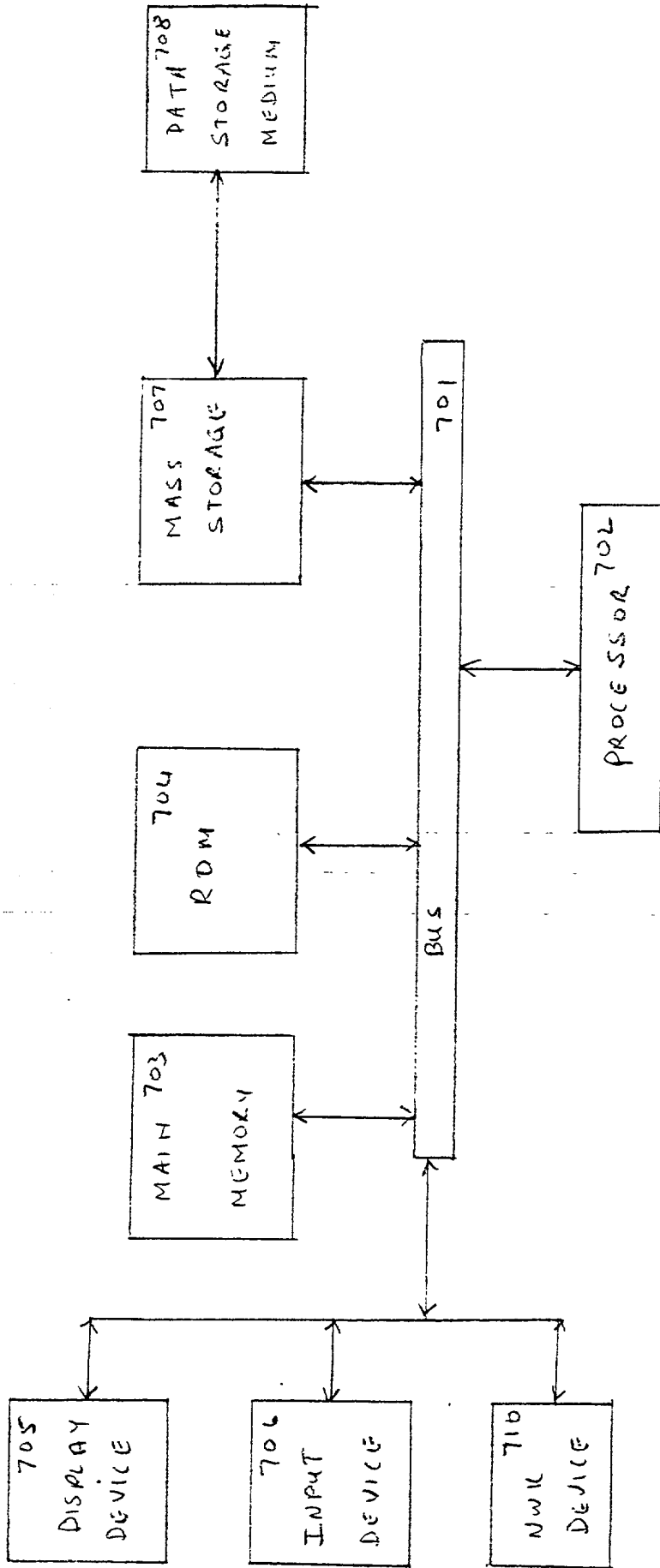


FIGURE 7